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FIG. 1 BACKGROUND ART

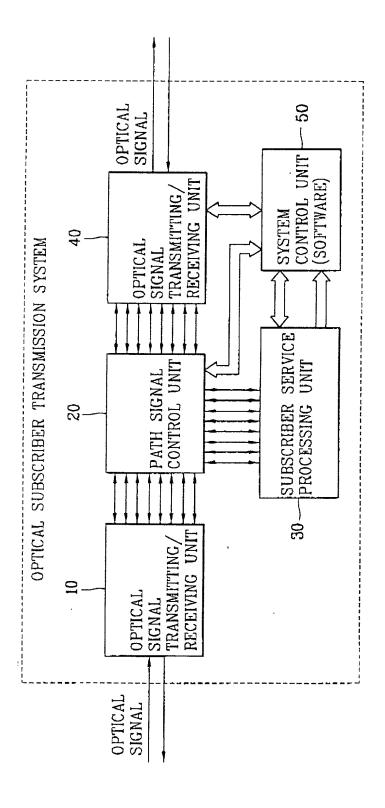
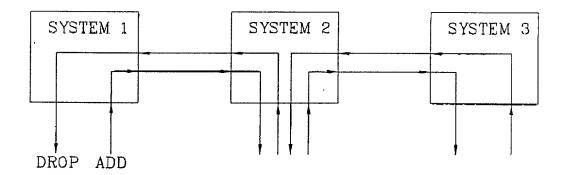
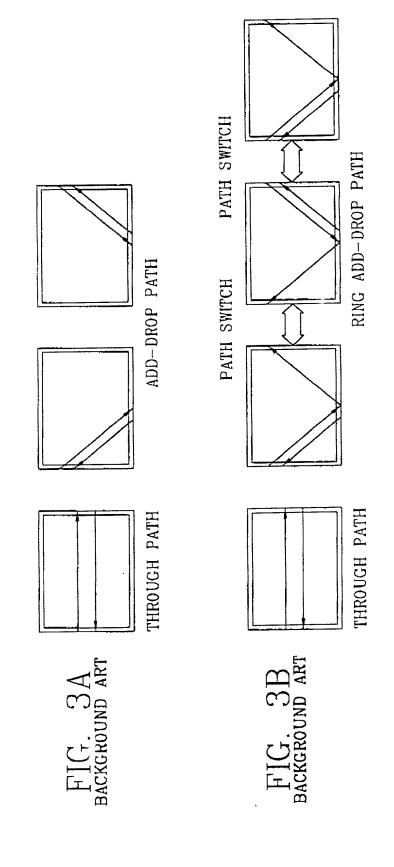


FIG. 2 BACKGROUND ART





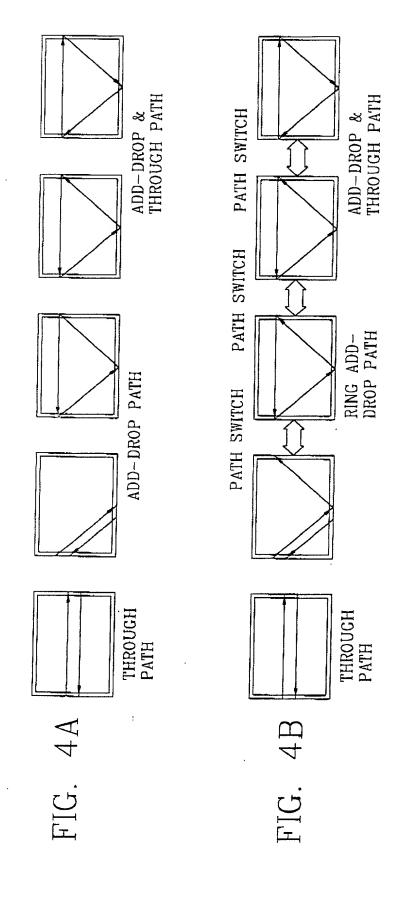


FIG. 5

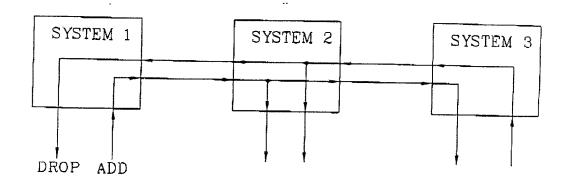
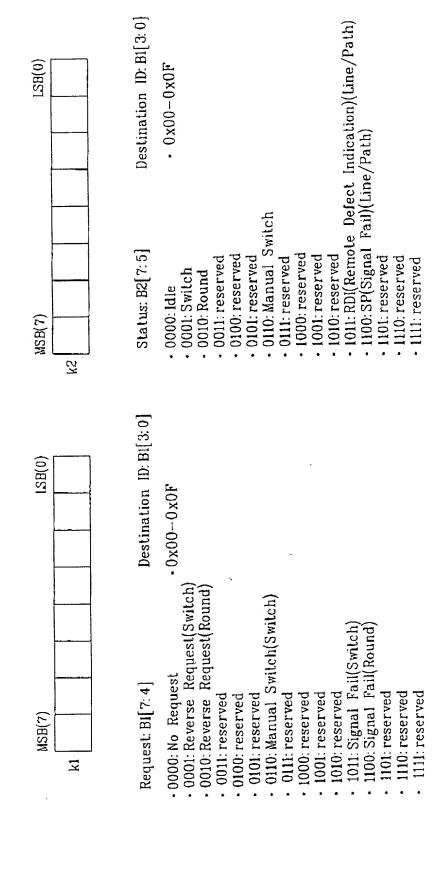


FIG. 6



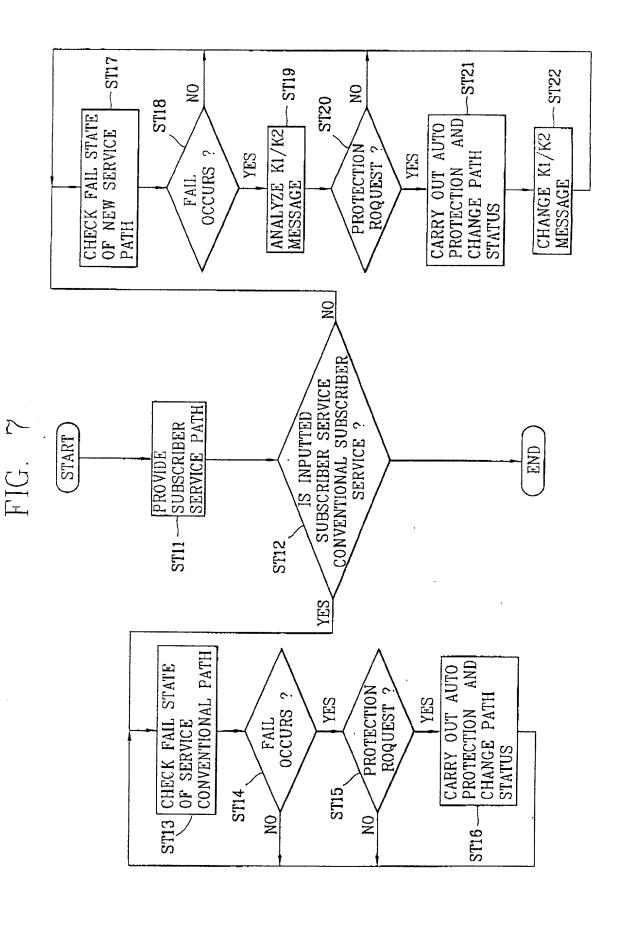


FIG. 8A

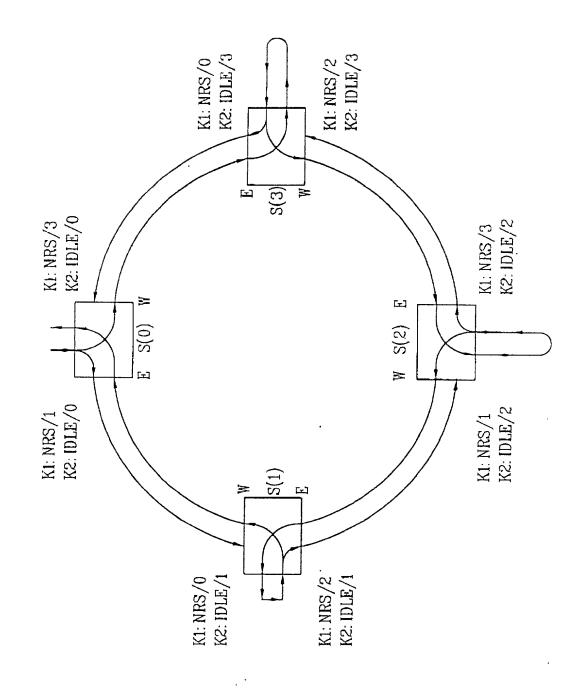


FIG. 8B

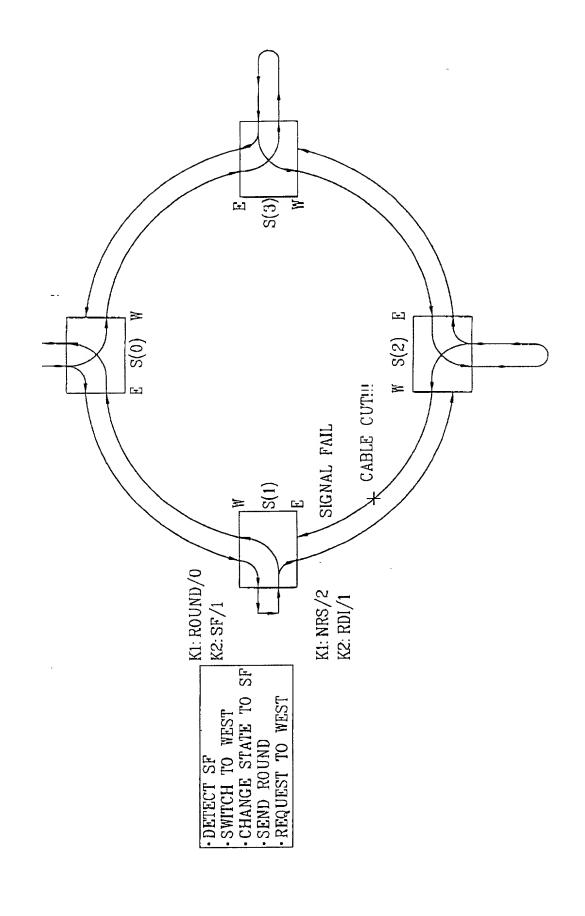


FIG. 8C

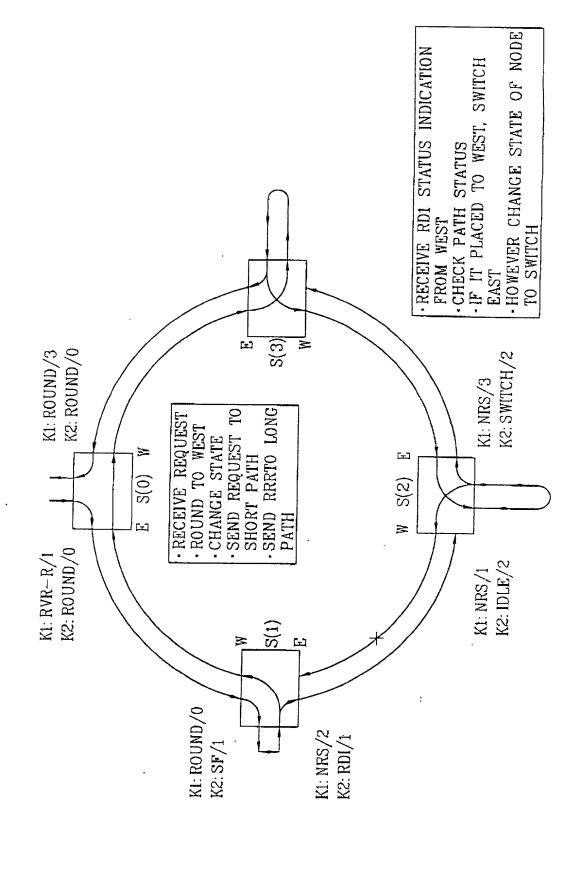


FIG. 8D

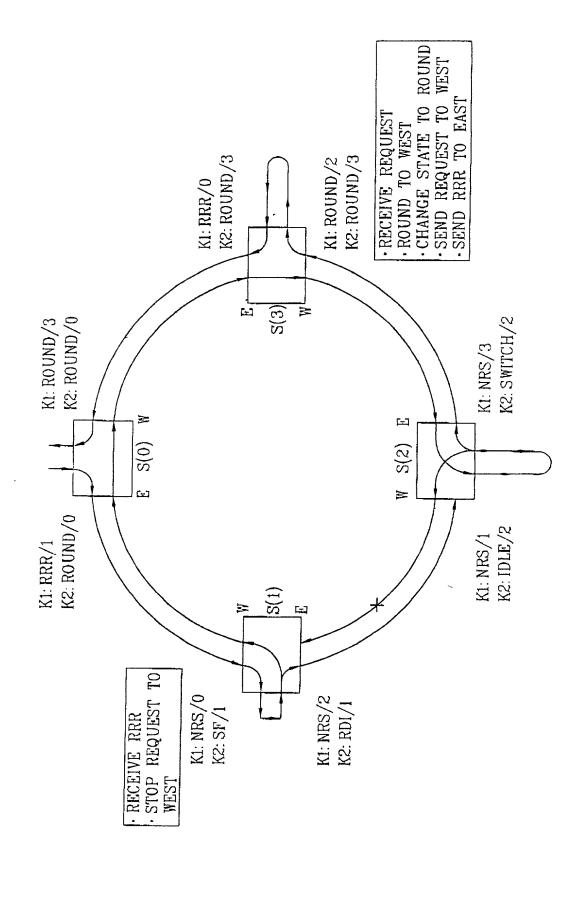


FIG. 8E

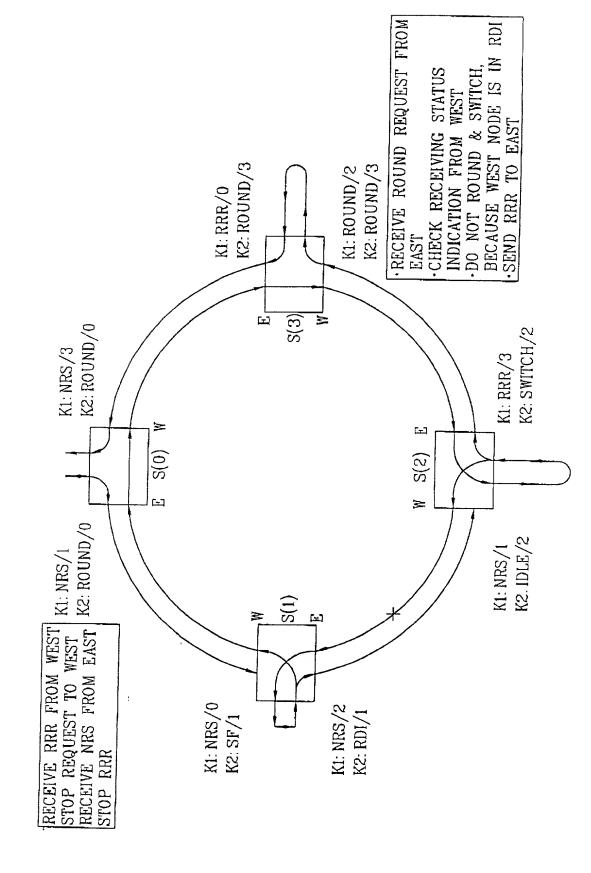
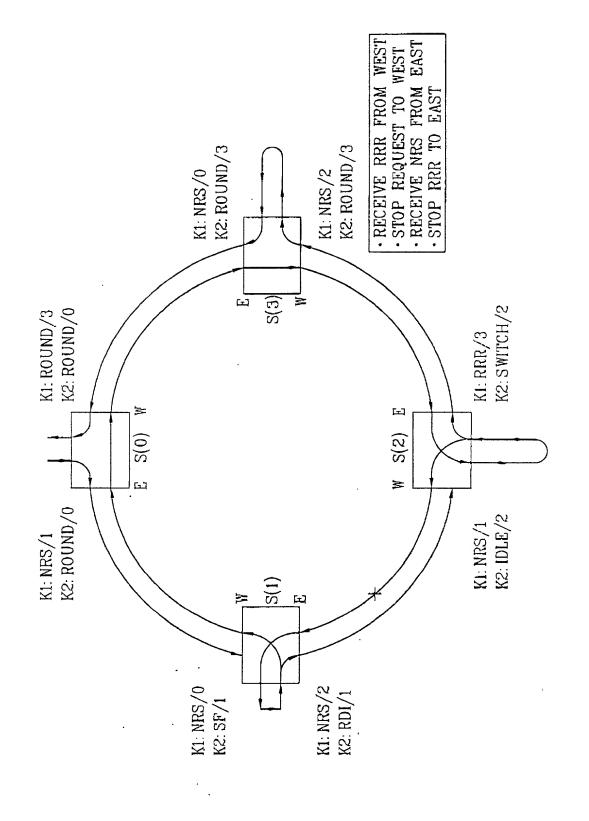


FIG. 8F



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FIG	

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s(z) s(S(1) S(\$(0) (83)		S(2)
K1: NRS/1 K2: IDLE/2	KI: NRS/0 K2: IDLE/2	K1: NRS/3 K2: IDLE/0	K1: NRS/2 K2: IDLE/3	
KI: NRS/2 SIGNAL K2: IDLE/1 FAIL	SIGNAL KI: NRS/1 FAIL K2: IDLE/0	KI: NRS/0 K2: IDLE/3	K1: NRS/3 K2: IDLE/2	
DETECT K1: NRS/2 PROTECTION K2: RDI/1	T K1: ROUND/0 IN K2: SF/1			
PROTECTION K1: NRS/1 K2: SWITCHED/2	RI: RRR/1 /2 K2: ROUNDED/0	KI: ROUND/3 K2: ROUNDED/0	KI: ROUND/3 KI: NRS/3 K2: ROUNDED/0 K2: SWITCHED/2	
		K1: RRR/0 K2: ROUNDED/3	PROTECTION	
K1: NRS/1 K2: SWITCHED/1	KI: NRS/0 K2: SF/0	KI: NRS/3 K2: ROUNDED/3	K1: NRS/2 K2: ROUNDED/2	
K1: NRS/2 K2: RDI/1	K1: NRS/1 . K2: ROUNDED/0	KI: NRS/0 K2: ROUNDED/3	K1: NRS/3 K2: SWITCHED/2	<u> </u>